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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/527,063	03/09/2005	Takeshi Shimoyama	267144US6PCT	4750

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EXAMINER	
ELAMIN, ABDELMONIEM I	
ART UNIT	**PAPER NUMBER**
2116	

NOTIFICATION DATE	DELIVERY MODE
10/01/2007	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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Office Action Summary	Application No.	Applicant(s)	
	10/527,063	SHIMOYAMA, TAKESHI	
	Examiner	Art Unit	
	Abdelmoniem Elamin	2116	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *19 June 2007*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-9* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-9* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

Claim Rejections - 35 USC § 102

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-9 are rejected under 35 U.S.C. 102(b) as being anticipated by Takemoto Japanese patent No. JP 408147163 A (cited by Applicant).

3. Claims 1, 7-9, Takemoto teaches an information processing apparatus operating in synchronism with a synchronizing clock signal of a predetermined frequency [*abstract, Fig. 1*], said information processing apparatus comprising:

clock outputting means for varying said frequency of said synchronizing clock signal in order to output said synchronizing clock signal at the varied frequency [*the operation clock could be set to different clock frequencies*];

holding means for inputting and holding data when said clock outputting means outputs a first clock signal pulse, said holding means further outputting said data held therein when said clock outputting means outputs a second clock signal pulse following said first clock signal pulse [*registers 15, 17, 19 of Fig. 1*];

selection command generating means for generating a selection command specifying whether or not to transfer said data by bypassing said holding means in accordance with the

frequency of said synchronizing clock signal output by said synchronizing clock outputting means [*controller 21 of Fig. 1*]; and

bypassing means for outputting said data by bypassing said holding means if said selection command generated by said selection command generating means specifies that said data be transferred by bypassing said holding means, said bypassing means further outputting said data output by said holding means if said selection command specifies that said data be transferred without bypassing said holding means [*bypassing lines of Fig. 1*].

4. Claim 2, Takemoto teaches a plurality of groups each made up of said holding means and said bypassing means connected in that order, said plurality of groups being connected in cascaded fashion [*see Fig. 1*].

5. Claim 3, Takemoto teaches data processing means for performing a predetermined process on said data; wherein said holding means inputs, holds, and outputs said data having undergone said process performed by said data processing means; and wherein said bypassing means outputs said data having undergone said process performed by said data processing means by bypassing said holding means if said selection command specifies that said data be transferred by bypassing said holding means, said bypassing means further outputting said data which, having undergone said process performed by said data processing means, was input to, held in, and output by said holding means if said selection command specifies that said data be transferred without bypassing said holding means [*data processing parts 13 and 14 of Fig. 1*].

6. Claim 4, Takemoto teaches stop controlling means for exercising control to stop processing of said holding means if said selection command generated by said selection

command generating means specifies that said data be transferred by bypassing said holding means [*abstract*].

7. Claim 5, Takemoto teaches said selection command generating means further generates frequency information corresponding to said frequency of said synchronizing clock signal output by said synchronizing clock outputting means, before generating said selection command based on the generated frequency information [*abstract*].

8. Claim 6, Takemoto teaches said selection command generating means further receives frequency information which is supplied from an external source and which corresponds to said frequency of said synchronizing clock signal output by said synchronizing clock outputting means, before generating said selection command based on the received frequency information [*see abstract and Fig. 1*].

9. Claims 1-9 are rejected under 35 U.S.C. 102(e) as being anticipated by Cook et al, US. Pat. No. 6,829,716.

10. Claims 1, 7-9, Cook teaches an information processing apparatus operating in synchronism with a synchronizing clock signal of a predetermined frequency [*abstract, col. 2, lines 26-58*], said information processing apparatus comprising:

clock outputting means for varying said frequency of said synchronizing clock signal in order to output said synchronizing clock signal at the varied frequency [*230 and 230` of Fig. 5*];

holding means for inputting and holding data when said clock outputting means outputs a first clock signal pulse, said holding means further outputting said data held therein when said

clock outputting means outputs a second clock signal pulse following said first clock signal pulse [*element 102 of Fig. 5*];

selection command generating means for generating a selection command specifying whether or not to transfer said data by bypassing said holding means in accordance with the frequency of said synchronizing clock signal output by said synchronizing clock outputting means [*col. 4, lines 52+*]; and

bypassing means for outputting said data by bypassing said holding means if said selection command generated by said selection command generating means specifies that said data be transferred by bypassing said holding means, said bypassing means further outputting said data output by said holding means if said selection command specifies that said data be transferred without bypassing said holding means [*see Fig. 5, col. 4, lines 52+*].

11. Claim 2, Cook teaches a plurality of groups each made up of said holding means and said bypassing means connected in that order, said plurality of groups being connected in cascaded fashion [*see col. 7, lines 50+*].

12. Claim 3, Cook teaches data processing means for performing a predetermined process on said data; wherein said holding means inputs, holds, and outputs said data having undergone said process performed by said data processing means; and wherein said bypassing means outputs said data having undergone said process performed by said data processing means by bypassing said holding means if said selection command specifies that said data be transferred by bypassing said holding means, said bypassing means further outputting said data which, having undergone said process performed by said data processing means, was input to, held in, and output by said

holding means if said selection command specifies that said data be transferred without bypassing said holding means [*see Fig. 7 and related disclosure*].

13. Claim 4, Cook teaches stop controlling means for exercising control to stop processing of said holding means if said selection command generated by said selection command generating means specifies that said data be transferred by bypassing said holding means [*col. 4, lines 52+*].

14. Claim 5, Cook teaches said selection command generating means further generates frequency information corresponding to said frequency of said synchronizing clock signal output by said synchronizing clock outputting means, before generating said selection command based on the generated frequency information [*see Fig. 5, col. 4, lines 41+*].

15. Claim 6, Cook teaches said selection command generating means further receives frequency information which is supplied from an external source and which corresponds to said frequency of said synchronizing clock signal output by said synchronizing clock outputting means, before generating said selection command based on the received frequency information [*see Fig. 5, col. 4, lines 41+*].

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Abdelmoniem Elamin whose telephone number is 571-2727-3674. The examiner can normally be reached on MON - THUR 10:00 AM - 6::00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Prrveen can be reached on 571-272-3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Abdelmoniem Elamin
Primary Examiner
Art Unit 2116

August 27, 2007